AN60303

PUMA SOM-RK3399-Q7 Design Notes
Baseboard Design for the **PUMA SOM-RK3399-Q7**

APPLICATION NOTE

Contents

1 Design Notes

This documents highlights a few important details when designing with the PUMA SOM-RK3399-Q7 module.

1.1 Ethernet Center Taps

The PUMA SOM-RK3399-Q7 module uses the Micrel KSZ9031RXN Gigabit Ethernet PHY.

As described in section 12.0 of the KSZ9031RXN data sheet, the PHY uses voltage mode transmit drivers that require separate center taps for each differential pair.
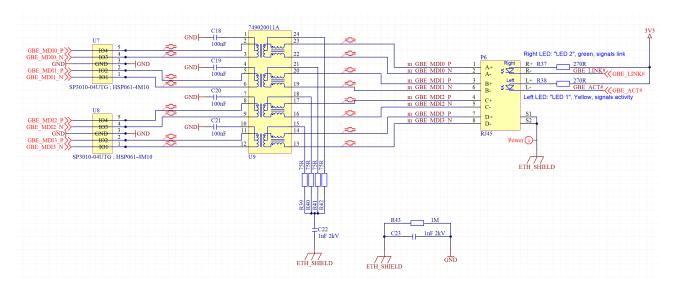
The suggested schematic is shown below.

Fig. 1.1: Suggested Ethernet transformer schematics

1.2 Ethernet with 100Mbit Transformer

Using an 100Mbit-transformer can cause slow link detect behavior when connected to a Gigabit port. The reason is that the PHYs on both ends will agree on Gigabit speed, while the actual transmission line is missing the two extra differential pairs that are required.

In U-Boot, you may get error like the one shown below:

```
dhcp
ethernet@fe300000 Waiting for PHY auto negotiation to complete........ TIMEOUT !
Could not initialize PHY ethernet@fe300000
```

It is recommended to either place a Gigabit-capable transformer on the baseboard or to explicitly disable Gigabit in software.

In U-Boot, you can limit the speed to 100Mbit by running:

```
setenv disable_giga 1
```

2 Contact

Cherry Embedded Solutions GmbH
Seestadtstraße 27
1220 Vienna
Austria

Inquiries: sales-es@cherry.de
Technical Support: support-es@cherry.de